

# SMART Memory Test Lab Report



MOTHERBOARD	MANUFACTURER	CHIPSET	TEST NUMBER	TEST STATUS
S5520HC	INTEL	INTEL 5520	SMTL0013	PASS

### Module Information

Manufacturer Name:	SMART MODULAR TECH	Module Type:	DDR3 REGISTERED ECC DIMM
Manufacturer P/N:	SG2567RD312893SF	CAS Latency:	C999
Memory Capacity:	2GB	Assembly Type:	SINGLE BOARD NON STACKED
Module Configuration:	6 PER CPU /12 TOTAL	Low Profile:	NO
DRAM Supplier:	SAMSUNG	Register P/N:	IDT IDTSSTE32882
DRAM Part Number:	K4B1G0846F-HCH9	PLL P/N:	N/A
PCB Part Number:	PH5AG240NEBHB2RB	Dual Die Package:	MONOLITHIC
PCB Layer Count:	10	Comment:	

### MINIMUM SYSTEM INFORMATION

System Name:	S5520HC
Serial Number:	31550MB0000
BIOS Version:	PHOENIX-SMART 8011.000
Processor Code:	INTEL XEON X5550
OS Memory Size:	12GB
OS Version:	LINUX SMP 2.6.28.10
Test Version:	HPL XLINPAK 10.2.3 MKL
Comment:	

### MAXIMUM SYSTEM INFORMATION

System Name:	S5520HC
Serial Number:	31550MB0000
BIOS Version:	PHOENIX-SMART 8011.0006
Processor Code:	INTEL XEON X5550
OS Memory Size:	24GB
OS Version:	LINUX SMP 2.6.28.10
Test Version:	HPL XLINPAK 10.2.3.MKL
Comment:	

### PRE-TESTING INFORMATION

Board Revision:	
Insertion Test:	PASS
SPD Check:	PASS
DDR Speed Check:	PASS
Boot Time:	1 MIN
<b>Voltage Margining Info</b>	
Volt %	+/-3%
Volt Low	1.45 volt
Volt High	1.55 volt

### TESTING DETAILS FOR VOLT LOW

Temperature:	55C	Time:	12HRS
Start Date:	5/5/2010		
Stop Date:	5/7/2010		
HPL Test Status:	PASS		
DDR Speed:	1333MHz		
Summary Min:	P		
Summary Max:	P		
Test Status:	PASS		

### TESTING DETAILS FOR VOLT HIGH

Temperature:	55C	Time:	12HRS
Start Date:	5/4/2010		
Stop Date:	5/10/2010		
HPL Test Status:	PASS		
DDR Speed:	1333MHz		
Summary Min:	P		
Summary Max:	P		
Test Status:	PASS		

### Serial Number List

1) SFR2010041300009	2) SFR2010041300006	3) SFR2010041300007	4) SFR2010041300004
5) SFR2010041300005	6) SFR2010041300002	7) SFR2010041300003	8) SFR2010041300001
9) SFR2010041300011	10) SFR2010041300010	11) SFR2010041300013	12) SFR2010041300012
13)	14)	15)	16)

### CONTACT INFORMATION

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Test Traveler Notes:

VERIFICATION

Verified by: Jim Schwarz

Test Technician: Mark Durand

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